



Application No. 10/675,606  
Date of Notice of Allowance: March 14, 2005  
Express Mailing Label No. EV460259853US

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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Application No. : 10/675,606  
Confirmation No. : 6342  
Applicants : Jeffrey F. DeNatale, et al.  
Filing Date : September 29, 2003  
Art Unit : 2817  
Examiner : Lee, Benny T.  
Atty. Docket No. : 00SC134US9  
Title (As amended) : LOW LOSS RF PHASE SHIFTER WITH FLIP-  
CHIP MOUNTED MEMS INTERCONNECTION

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Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attention: Official Draftsperson

**TRANSMITTAL OF CORRECTED DRAWINGS**

Sir:

Enclosed in response to the Notice of Allowance mailed March 14, 2005 in connection with the above application, is a full set of corrected, formal drawings.

Respectfully submitted,

Dated: March 31, 2005

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Louis A. Mok  
Registration No. 22,585

Attorney for Applicants

Attachments  
KOPPEL, JACOBS, PATRICK & HEYBL  
555 St. Charles Drive, Suite 107  
Thousand Oaks, California, 91360

Telephone: (805) 373-0060



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CERTIFICATE OF EXPRESS MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service, Express Mail Label EV460259853US, in an envelope addressed to: Mail Stop Issue Fee, Commissioner of Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

March 31, 2005  
Date

Eleanor Nakada  
Eleanor Nakada